

FIG. 1B

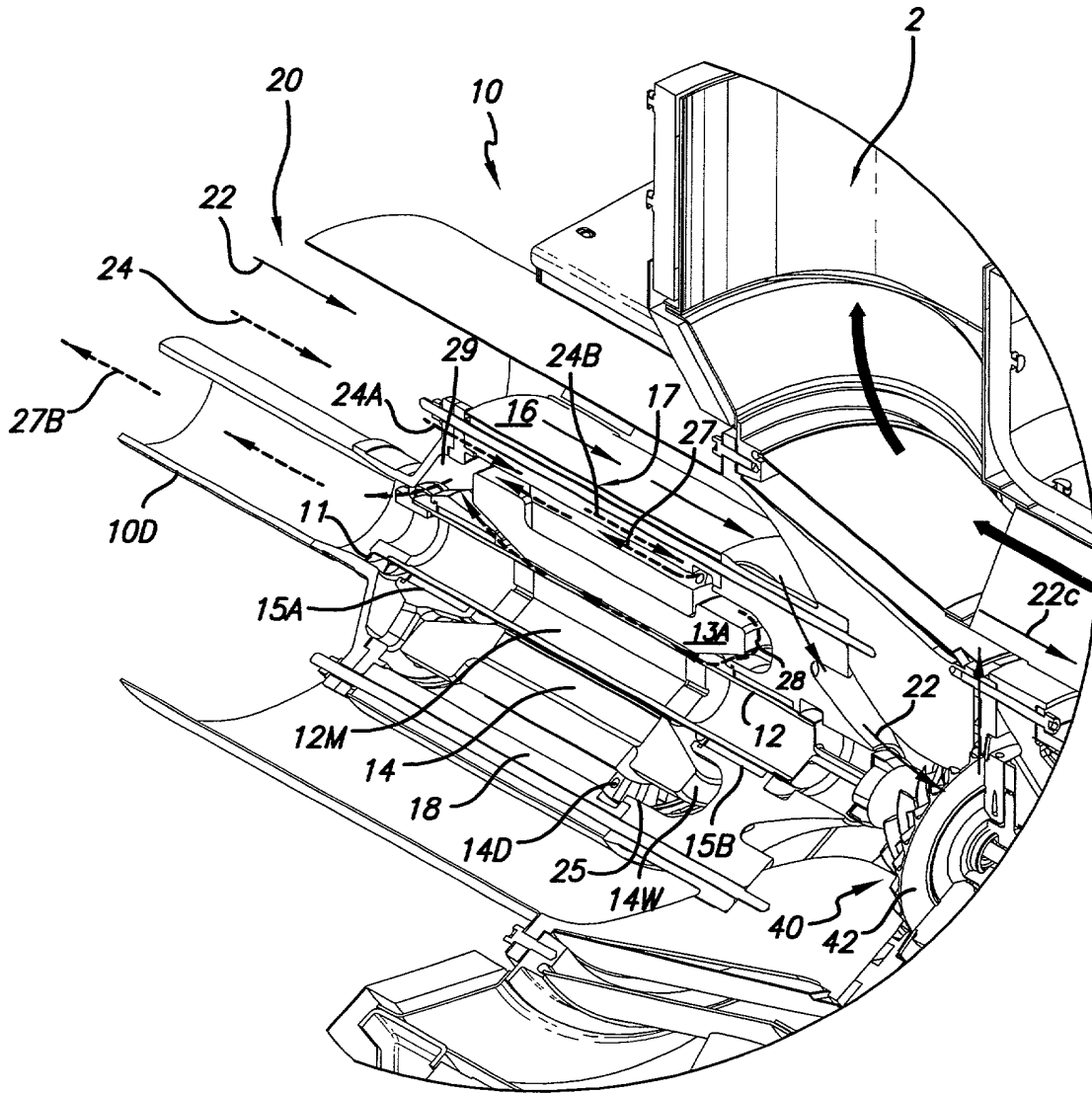


FIG. 1C

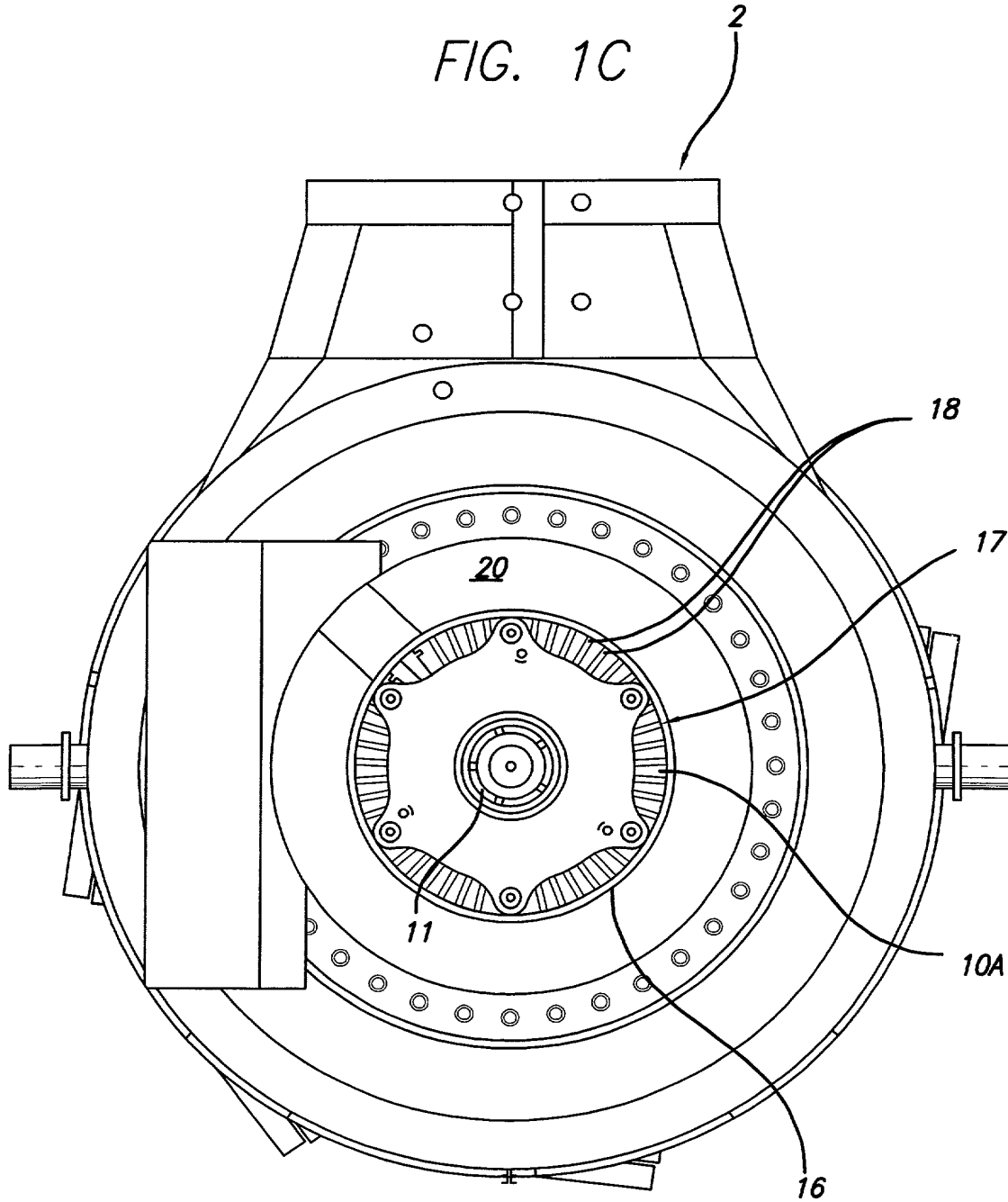


FIG. 1D

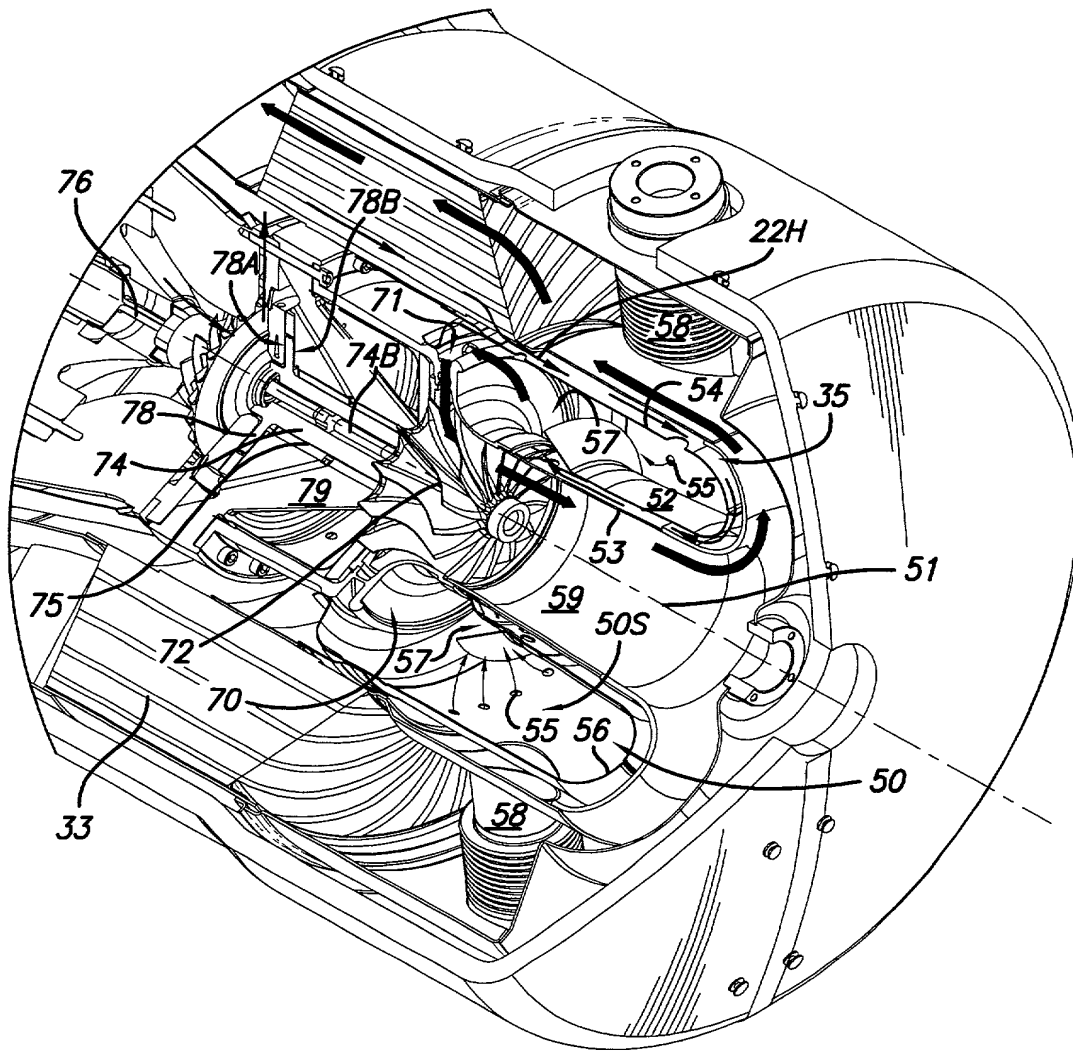


FIG. 1D

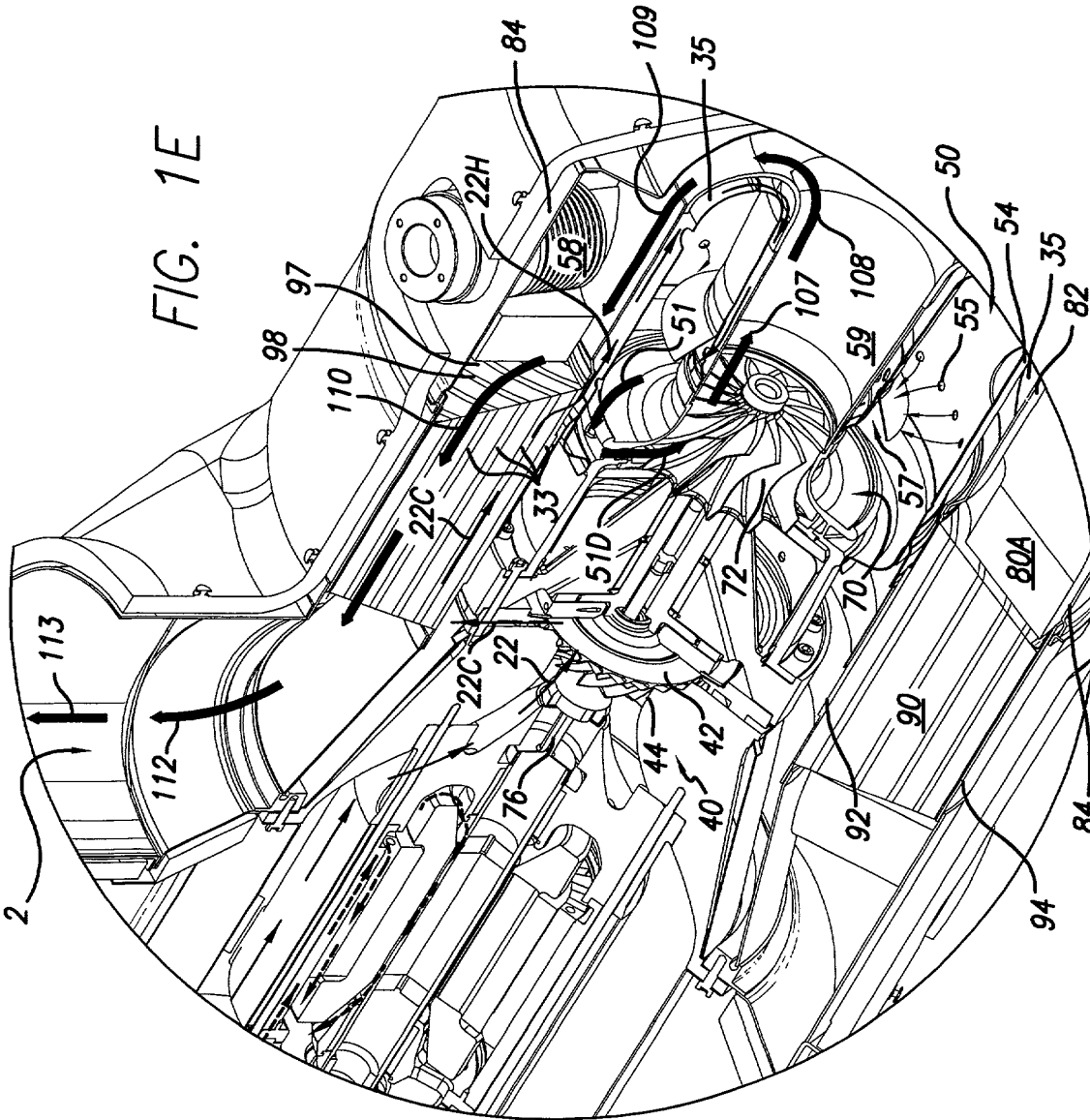
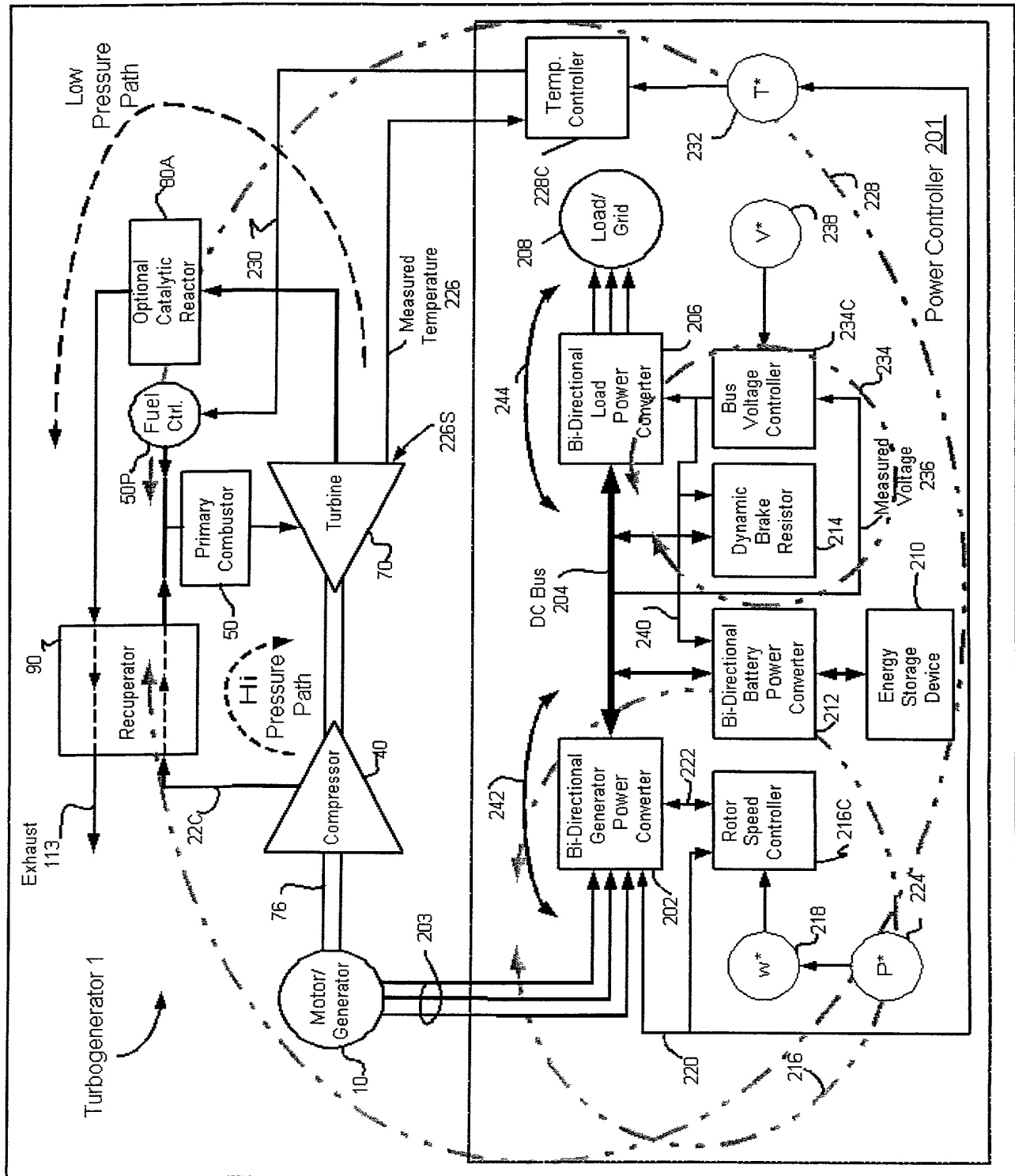


Fig. 2



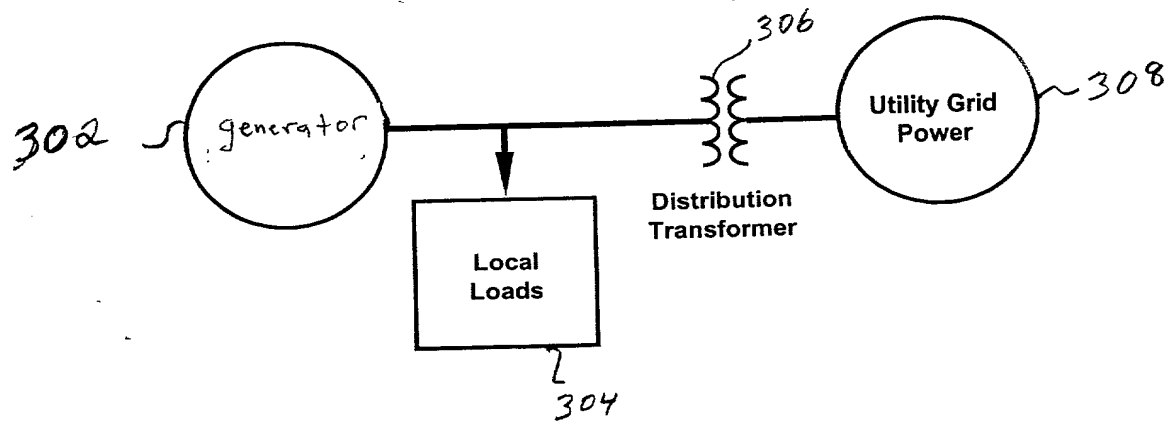


Figure 3

FIG. 3 OF 14

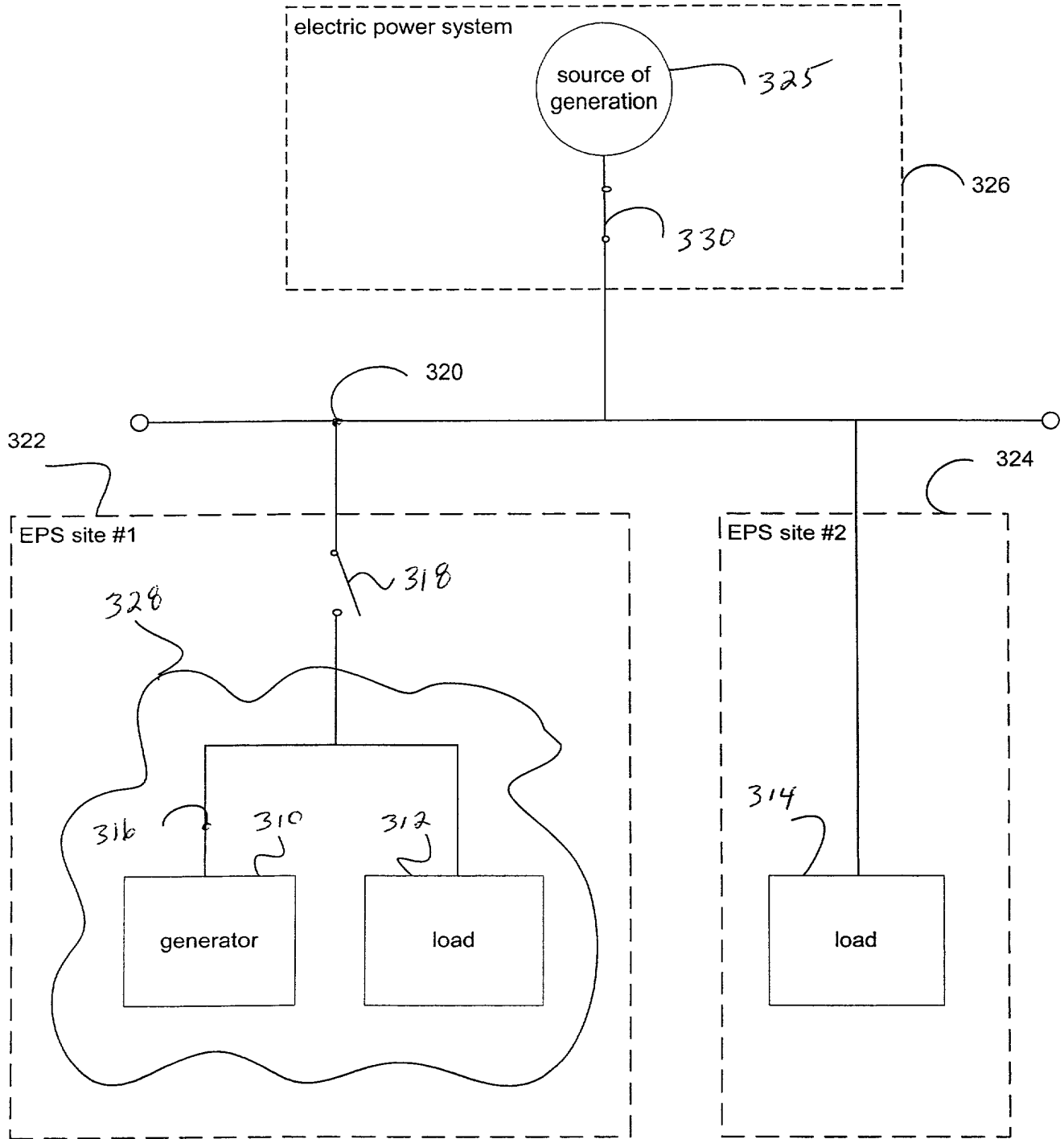


FIGURE 4

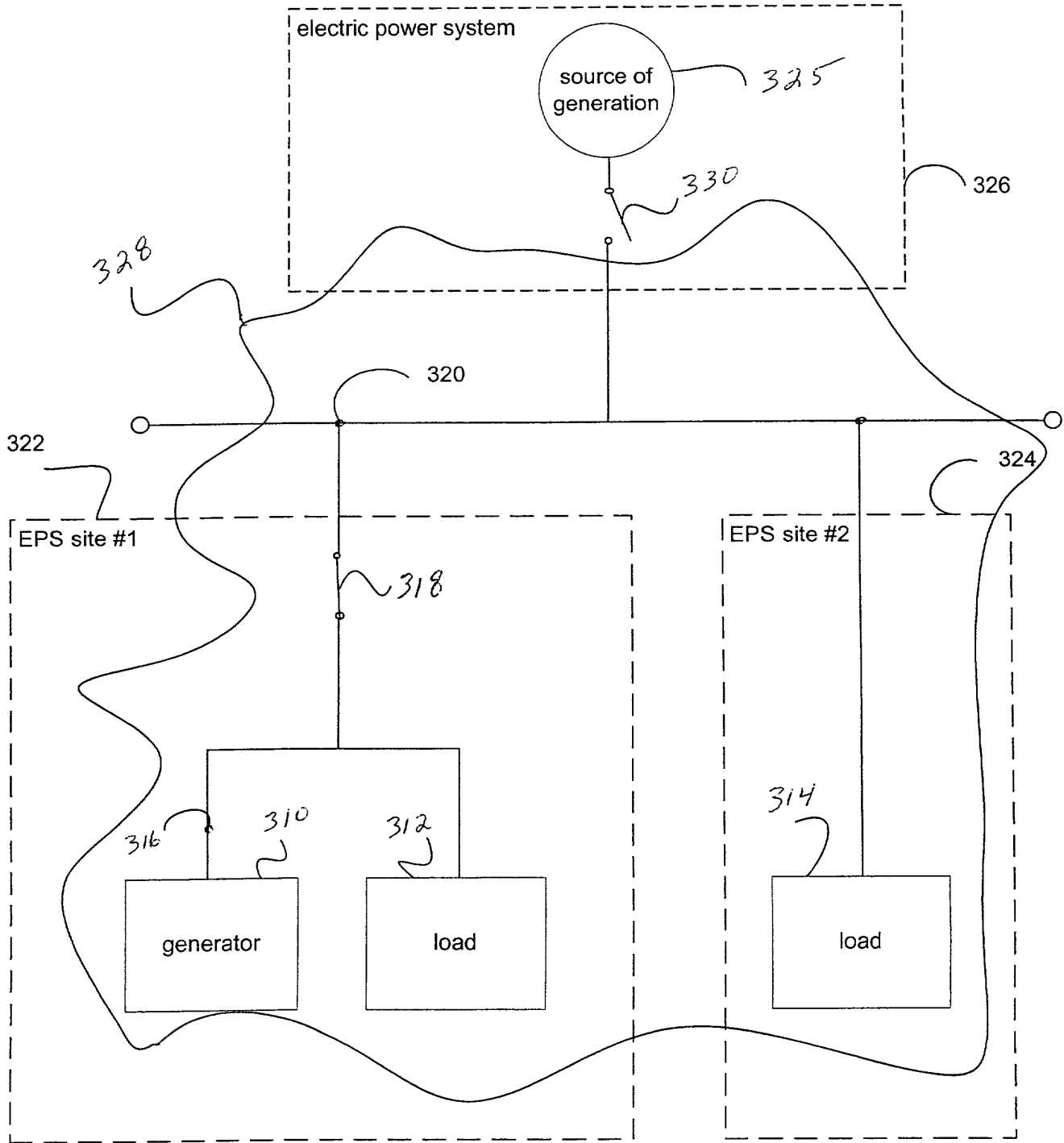


FIGURE 5

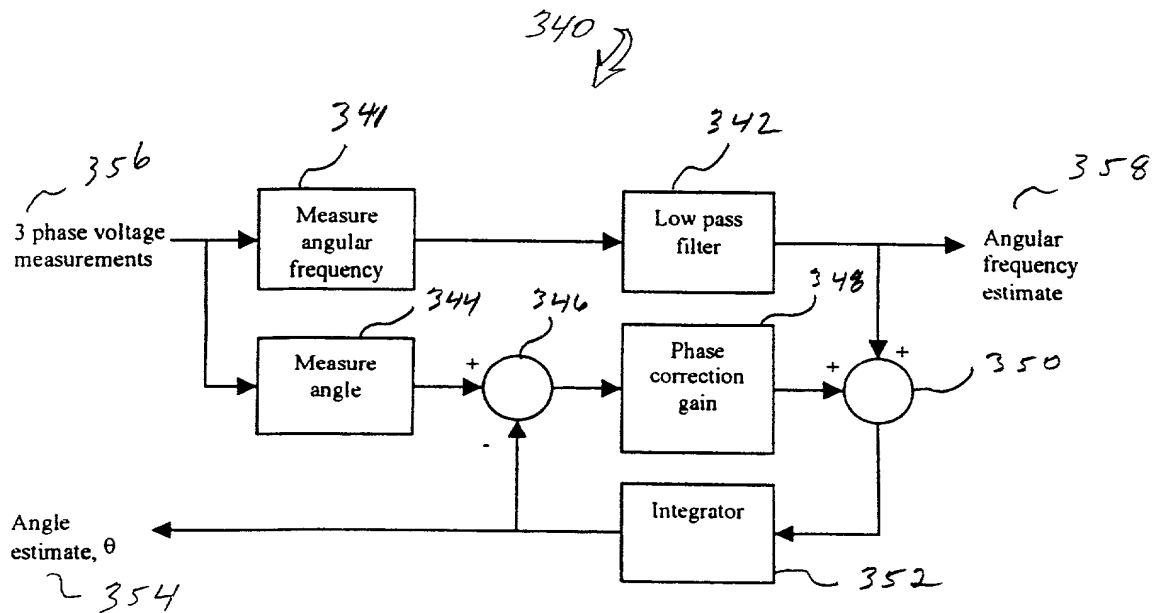
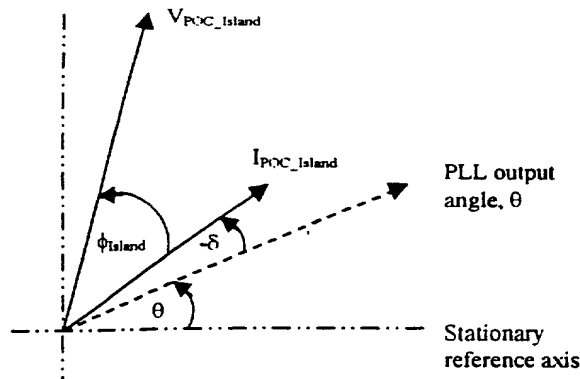


Figure 6



I_{POC_island}	Islanded DR generator current magnitude at the POC (A)
V_{POC_island}	Islanded voltage magnitude at the POC (V)
Z_{island}	Impedance magnitude of the island looking into the POC (Ω)
ϕ_{island}	Impedance phase-angle of the island looking into the POC (radians)
P_{DRG}	Demanded real power output of the DR generator (W)
Q_{DRG}	Demanded reactive power output of the DR generator (lagging is positive) (VAr)
θ	Angle output from the PLL (radians)
δ	Demanded current phase angle, $\delta = \tan^{-1}(Q_{DRG}/P_{DRG})$ (radians)

Figure 7

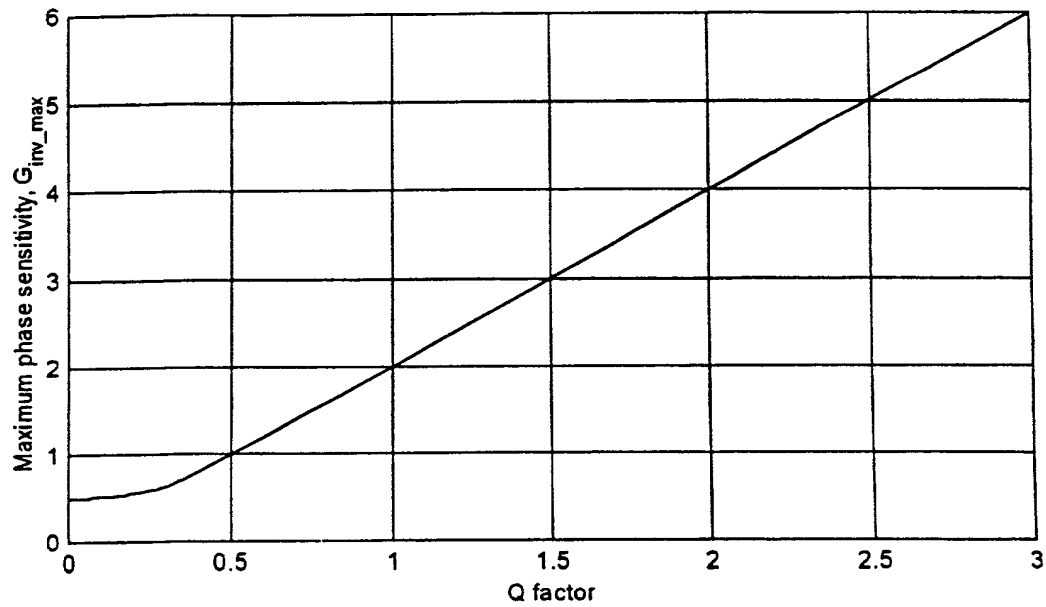


Figure 8

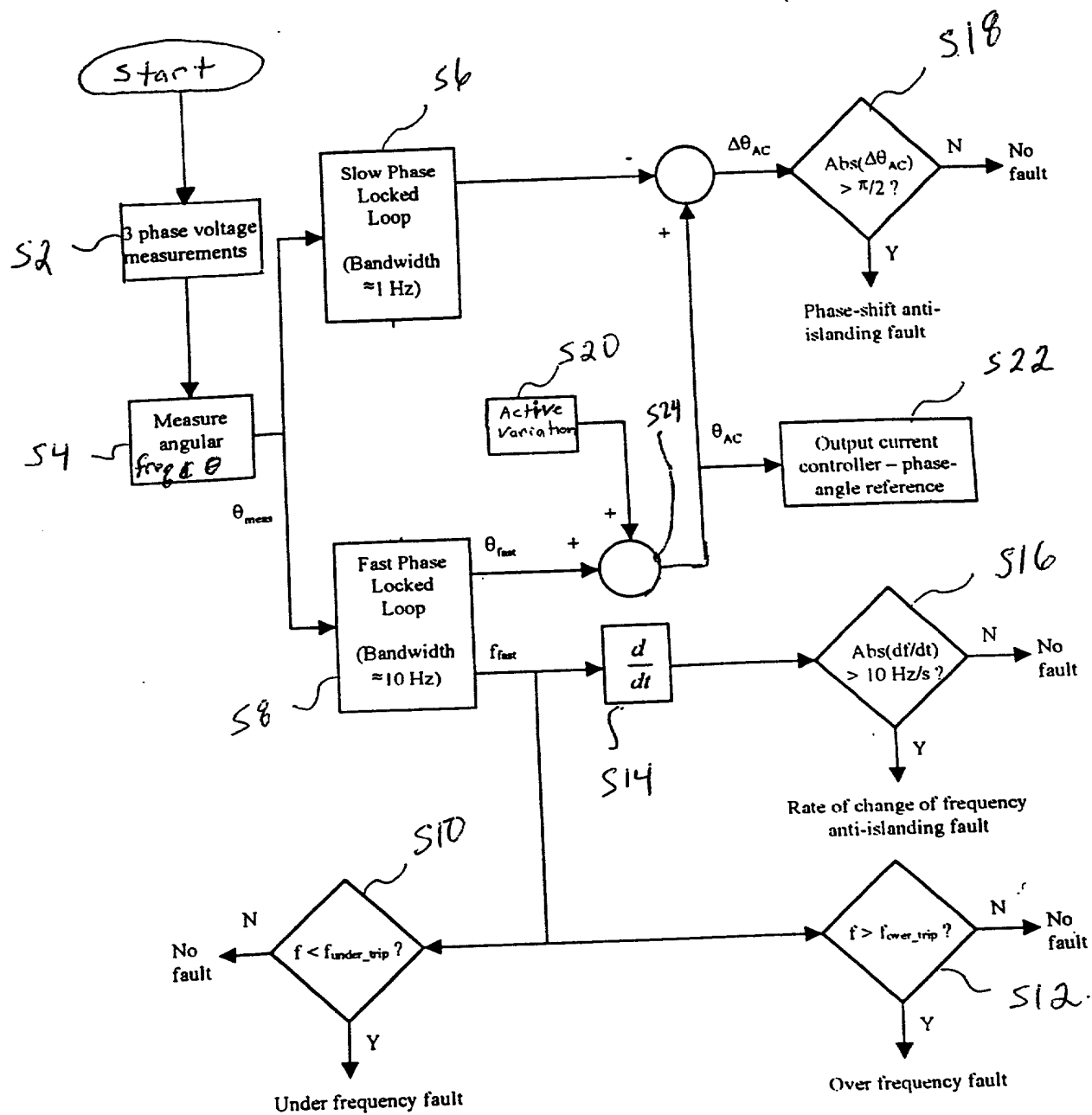


Figure 9A

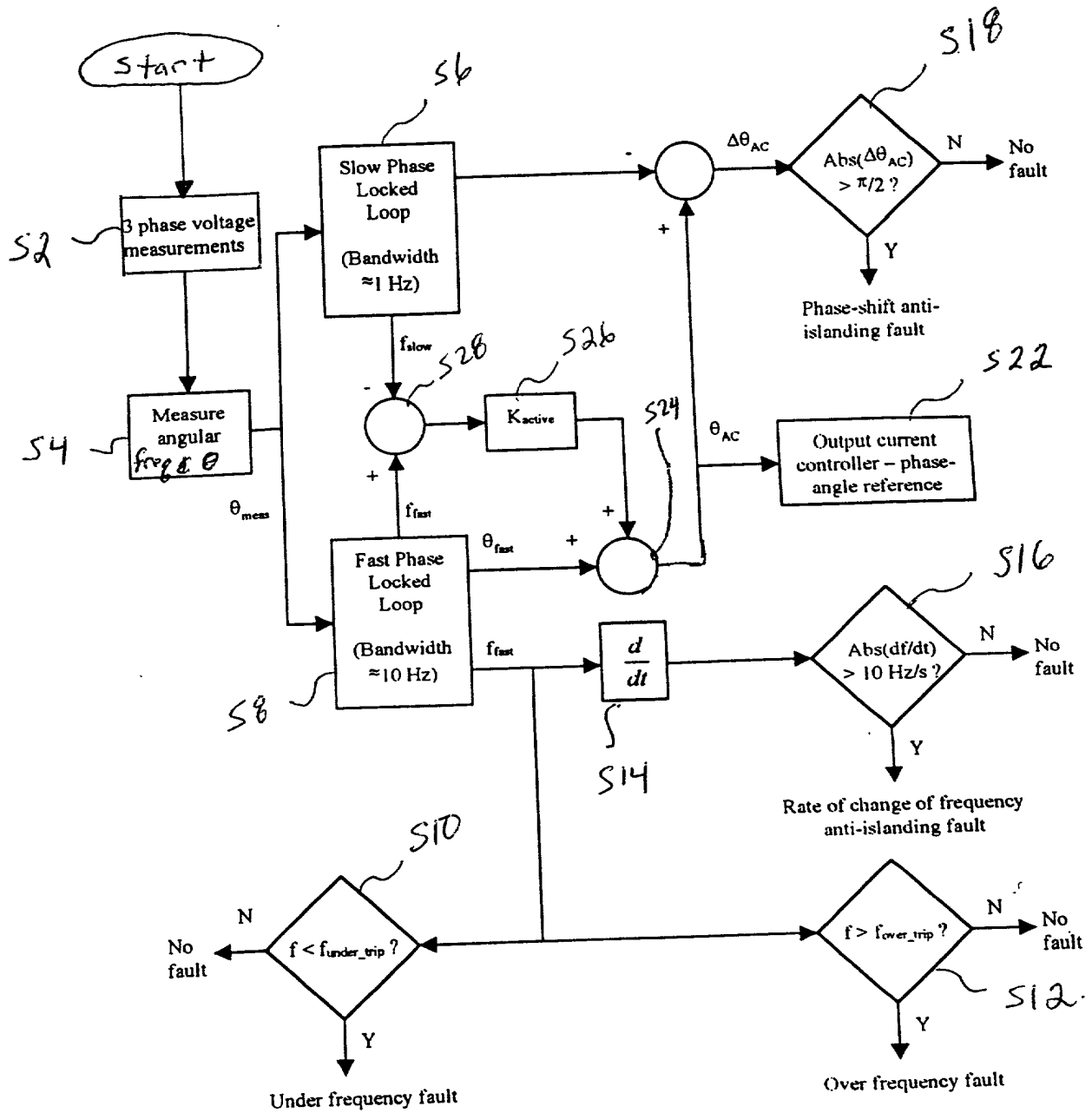


Figure 9B

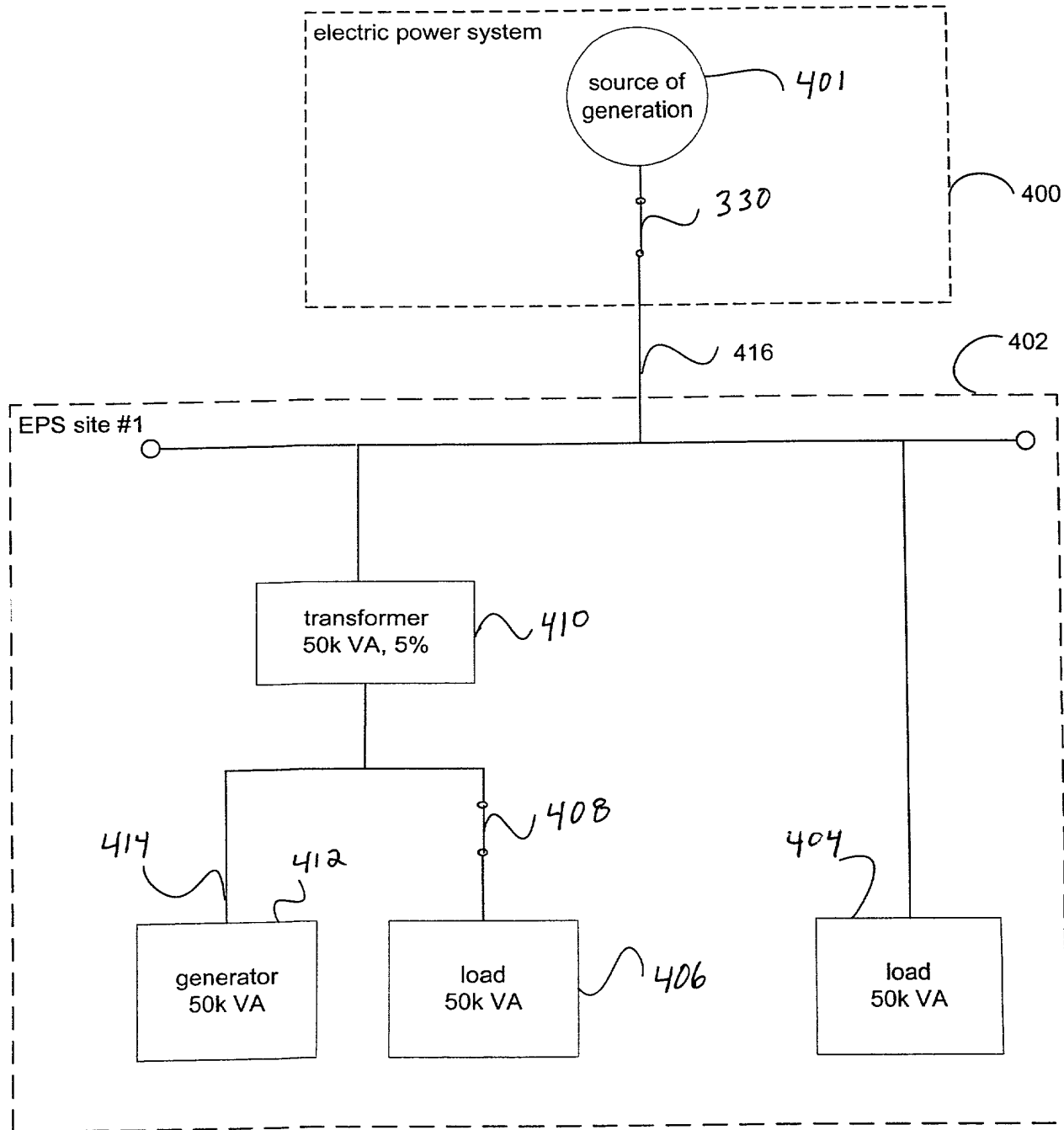


Figure 10